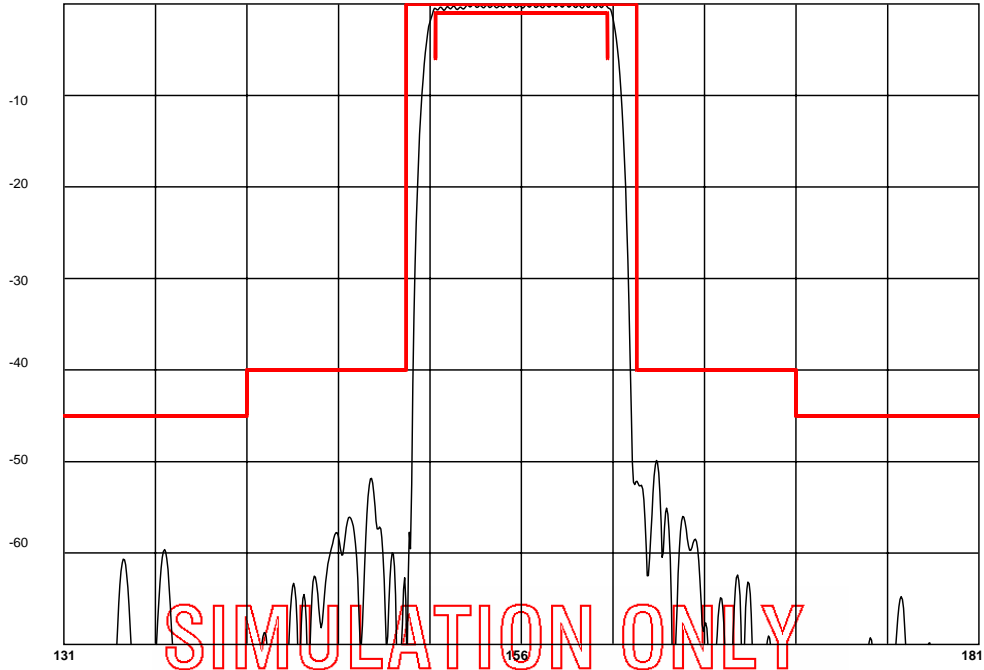
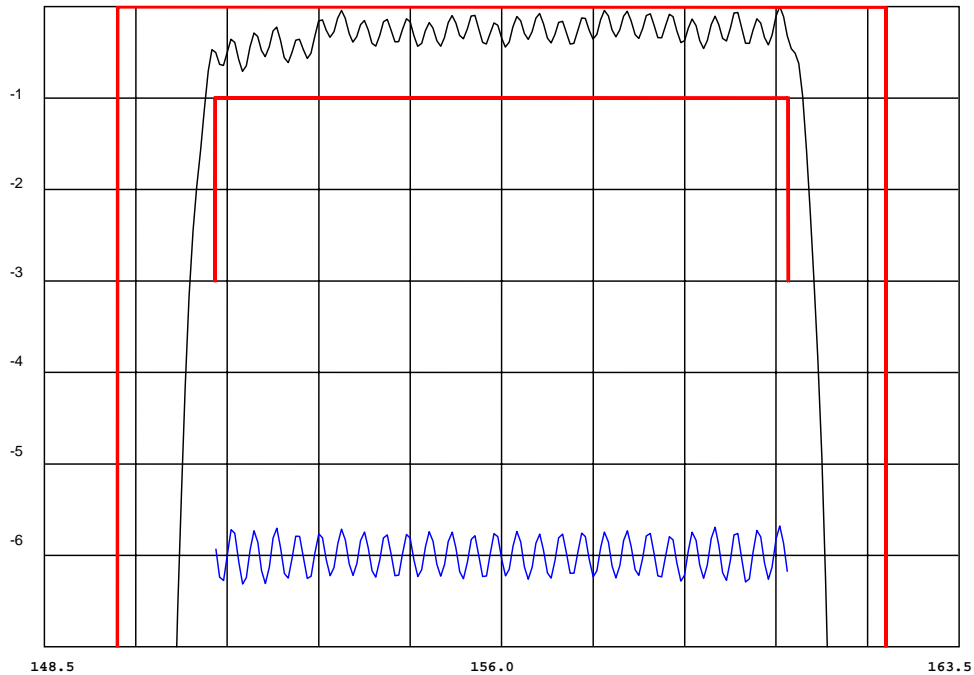




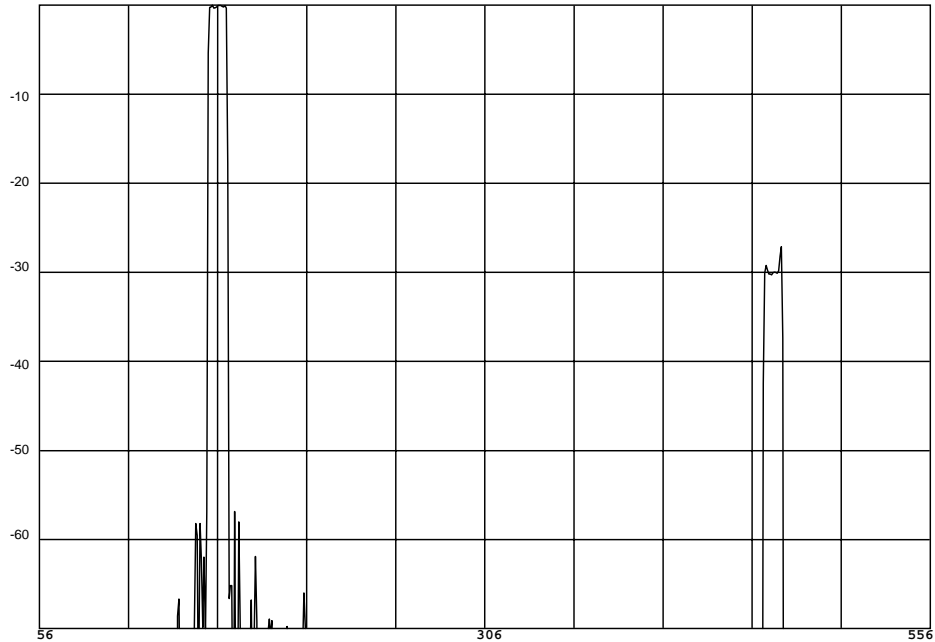
**TYPICAL PERFORMANCE**



Horizontal: 5 MHz/div      Vertical (from top): Magnitude      10 dB/div



Horizontal: 1.5 MHz/div      Vertical (from top): Magnitude      1 dB/div  
Group Delay Deviation      150 ns/div



Horizontal: 50 MHz/div Vertical (from top): Magnitude 10 dB/div

**SIMULATION ONLY**

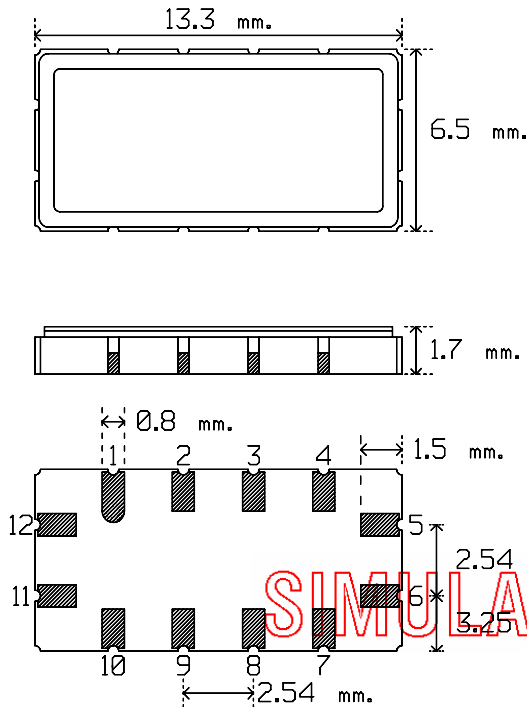
**SPECIFICATION**

Parameter	Min	Typ	Max	Units
Center Frequency (Fc) <sup>1</sup>		156.00		MHz
Minimum Insertion Loss <sup>2</sup>		20.6	22.0	dB
1 dB Bandwidth	9.40			MHz
Amplitude Variation (151.3 to 160.7 MHz)			1.0	dB p-p
Group Delay Ripple (151.3 to 160.7 MHz)			150	ns p-p
Relative Attenuation at 141.0 MHz	45			dB
Relative Attenuation at 149.7 MHz	40			dB
Relative Attenuation at 162.3 MHz	40			dB
Relative Attenuation at 171.0 MHz	45			dB
Ultimate Rejection	50			dB
Source and Load Impedance		50		$\Omega$
Input Power		+10		dBm
Operating Temperature Range	-10	23	65	$^{\circ}$ C

- Notes:
1. Average of the lower and upper 3 dB band edge frequencies.
  2. All dB values are referenced to the insertion loss.
  3. Specifications are valid for a temperature of 23C.



**PACKAGE OUTLINE**



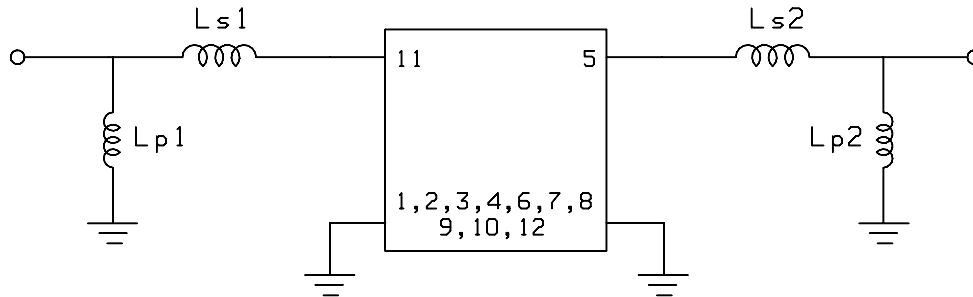
**Units: mm**

**Pin Configuration:**

Input: 11  
Output: 5  
Ground: 1,2,3,4,6,7,8,9,10,12

**SIMULATION ONLY**

**MATCHING CIRCUIT**



Typical component values:       $L_{s1} = 33 \text{ nH}$                $L_{p1} = 15 \text{ nH}$   
     $L_{s2} = 33 \text{ nH}$                $L_{p2} = 22 \text{ nH}$

(Minimum inductor Q = 40)

**Notes**

- Maximum 2% tolerance matching components shall be used.
- Tuning values shown are for reference only. Optimum values may change depending upon board layout.

